

Faculty Profile



Name: Dr. P ADAVALA AKHENDRA KUMAR

Designation: Assistant Professor

Teaching Areas: VLSI, Signal Processing and Machine learning

Research Interests: Analog/RF VLSI

Education:

- Ph.D, from National Institute of Technology Warangal in 2018
- M.tech from GMRIT(JNTU K) in 2011
- B.tech from BVCITS(JNTU H) in 2008.

Research / Selected Publications:

Journals-12

1. **P.Akhendra Kumar**, A N Kiran, N.Bheema Rao, High quality factor fractal inductor with complementary split-ring array inclusion, Circuit World, Jan 2020 , Pages 1–6, DOI:10.1108/CW-06-2019-0052 (SCI).
2. **P.Akhendra Kumar**, N.Bheema Rao, Series stacked fractal inductor for Radio frequency Applications, IET Electronics Letters, Volume 53, Oct 2017, Pages 1387–1388, DOI : 10.1049/el.2017.2623(SCI).
3. **P.Akhendra Kumar**, N.Bheema Rao, Fractal spiral capacitor for Wireless Applications, IET Electronics Letters, Volume 52, April 2017, Pages 481–483, DOI:10.1049/el.2015.3420 (SCI).

Conferences-14

1. **Keerthi Somaraj, P Akhendra kumar**, Analysis and Implementation of a Multi-path and Metal Stacked 8-Shaped Inductor, Springer Lecture Notes in Electrical Engineering, Vol 992, May 2023, PP,27-35.
2. **P.Akhendra Kumar**, N.Bheema Rao, High Q miniature fractal inductor based 2.4 GHz Low Noise Amplifier, ICIET - 2022(Presented)
3. **Keerthi Somaraj, Balamani Shetty, Akhendra kumar**, Design Verification of Low Noise Amplifier in L-Band, ICIET - 2022(Presented).